

**Amendments to the Specification:**

Please replace the paragraph beginning at page 8, line 23, with the following redlined paragraph:

The CPU, that is classically a hard-wired logic state machine or a microprogrammed logic machine, is designed for saving the contextual data present in the registers CCR, ACC, X, PCH, PCL, ~~CPE~~PCE in accordance with a stack management method according to the present invention.